

## DEVICE FOR LIMITING CURRENT IN A SENSOR

### BACKGROUND OF THE INVENTION

#### Field of the Invention

The present invention relates to the field of magnetic data read/write operations, and more particularly, to a device to limit 5 current to a read sensor during write operations.

#### Discussion of the Related Art

Disk drives employ giant-magneto-resistive ("GMR") sensors while performing reading operations on a disk. During read operations, current flows to the GMR sensor from the pre-amplifier.

10 Problems may occur when magnetic instability is experienced within the GMR sensor. Magnetic instability can cause irrecoverable damage to the GMR sensor that degrades performance and durability. Magnetic instability may occur when the current through the sensor exceeds a given threshold for a long duration.

15 The current in a GMR sensor may be increased inadvertently due to cross-coupling from the writer circuit coupled to the pre-amplifier. This cross-coupling may be due to magnetic fields induced by currents in the circuit loop for the writer signal path that couples inductively onto the circuit loop formed by the reader 20 signal path. This type of cross-coupling is called mutual inductance. Cross-coupling also may be due to electric fields induced by voltage potentials on the writer signal path that couple capacitively onto the

09846533-042304

P000000000000000

reader signal path. This type of cross-coupling is called mutual capacitance. The types of cross-coupling are not mutually exclusive, and both may occur at the same instance.

Fig. 1 depicts read and write signal paths to a recording head.

5      Pre-amplifier 102 supplies current to recording head 104 within a disk drive. A write circuit loop and a read circuit loop couple pre-amplifier 102 and recording head 104. The write circuit loop includes write driver 106, write coil 110 and write signal path 114. The read circuit loop includes read amplifier 108, GMR sensor 112

10     and read signal path 116. The read signal path and write signal paths that connect the preamp to the head are constructed out of a single piece of flexible printed circuit board that is referred to at the flex 130.

Mutual inductance may occur as follows. A current  $I_2$  flows

15    through writer signal path 114. Current  $I_2$  induces a magnetic field about write signal path 114. This magnetic field, in turn, induces a voltage  $VL_1$  in read signal path 116. Thus, currents  $I_2$  in write signal path 114 and voltages  $VL_1$  in read signal path 116 are coupled together.

20     Mutual capacitance may occur as follows. Voltage potentials on writer signal path 114, such as  $VL_2$ , induce electric fields. The electric fields create a voltage potential,  $V_c$ , between the two signal paths and induce a current,  $I_C$ , to flow into read signal path 116 from write signal path 114. Induced current  $I_C$ , in turn, becomes

25    current  $I_1$  in read signal path 116.

Both mutual inductance and mutual capacitance can induce current  $I_1$ . Current  $I_1$  also flows through the GMR sensor 112 and becomes sensor current  $Im_r$ . If sensor current  $Im_r$  already exists, it

may be increased by current I1. High current levels can damage GMR sensor 112. Currents greater than 10mA can cause potential instability in the GMR sensor 112. Further, the magnitude of the current that couples from writer signal path 114 into read signal path 116 will continue to increase as the rise-time of the write signal decreases. The rise-time of the write signal decreases because the rise-time is required to scale inversely with the ever-increasing data rate. The rise-time decrease is desired so that the write current has ample time to induce a field from the thin-film write head onto the magnetic media before the next data transition occurs. Thus, as the data rate increases, the read element will become prone to damage due to coupling from the writer.

One known approach to reduce the coupling effects includes spacing the two sets of signal paths further apart and keeping the individual pairs of signal lines for each signal path closer together. By spacing the read and write signal paths further apart, the capacitance between the two signal paths may be reduced. In addition, the mutual inductance between the two trace loops may be reduced. The mutual inductance may be reduced further by putting the two signal lines of the read signal path 116 closer together. The mutual inductance may also be reduced further by putting the two signal lines of the write signal path 114 closer together. Reducing the area created by each signal path. Shortcomings with this approach include an increased cost associated with the increased area, or width, of the overall read/write circuit as well as the increased cost of using finer geometries for manufacturing the closer-spaced signal pairs of each signal path.

09640622 - 072321

Another approach involves placing a conductive layer on the back side of the flex. This backing layer has the effect of reducing capacitive and magnetic coupling between the two pairs of signals. Shortcomings with this approach include an increased cost related  
5 to the more complex manufacturing process of the flex.

Another approach has been adding guard traces between the interconnects. The guard traces act to shield the electric and magnetic fields between the signal paths. Due to the limited area available on the flex these guard traces are ungrounded. A shortfall  
10 with adding the ungrounded traces, however, is that charging of these shield lines may impart the additional risk of an electro-static discharge event that could couple into the read lines and result in damage to the GMR sensor.

Thus, attempts to limit the current flowing to the GMR sensor  
15 during write operations result in increased costs, increased area or additional risks.

### SUMMARY OF THE INVENTION

To achieve these and other advantages and in accordance with the purpose of the present invention, as embodied and broadly  
20 described, a device for limiting current to a sensor is disclosed.

According to an embodiment of the present invention, a read/write device for a disk drive is disclosed. The read/write device includes a pre-amplifier and a recording head. The read/write device also includes a write signal path between the pre-amplifier  
25 and the recording head. The write signal path includes a write current. The read/write device also includes a read signal path between the pre-amplifier and the recording head. The read signal

path includes an induced current related to the write current. The read/write device also includes a shunt path in the pre-amplifier to draw a part of the induced current from the read signal path.

According to another embodiment of the present invention, a  
5 method for limiting a sensor current in a magneto-resistive sensor is disclosed. The method includes inducing a current in a read signal path coupled to said sensor. The method also includes shunting the current from the read signal path with a shunt path. The shunt path is opposite the sensor on the read signal path.

10 It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

#### BRIEF DESCRIPTION OF THE DRAWINGS

15 The accompanying drawings, which are included to provide further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention. In the drawings:

20 FIG. 1 illustrates read and write signal path to a recording head; and

FIG. 2 illustrates a read/write device in accordance with an embodiment of the present invention.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

25

Reference will now be made in detail to the preferred embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

Fig. 2 depicts a read/write device 200 in accordance with an embodiment of the present invention. Read/write device 200 includes pre-amplifier 202 and recording head 204. Read/write device 200 may be used within a disk drive and reads and writes to a magnetic storage medium, preferably a disk. Recording head 204 uses write coil 210 and GMR sensor 212 to perform these operations.

Pre-amplifier 202 includes a write driver 206 and a read amplifier 208. Write coil 210 is coupled to write driver 206 by write signal path 214, and GMR sensor 212 is coupled to read amplifier 208 by read signal path 216.

Shunt path 220 is coupled to read signal path 216 at the input to read amplifier 208. Preferably, shunt path 220 is placed on an opposite end of read signal path 216 from GMR sensor 212. Shunt path 220 may be comprised of sets of transmission gate 224 comprised of transistors 232 and 234, and transmission gate 226 comprised of transistors 236 and 238. Transmission gate 226 may be comprised of an n-channel transistor as well as a p-channel transistor. More particularly, transmission gate 226 may be constructed using an n-channel metal-oxide-semiconductor ("nMOS") transistor 232 as well as a p-channel metal-oxide-semiconductor ("PMOS") transistor 234. Transmission gate 224 may be comprised of an n-channel transistor as well as a p-channel transistor. More particularly, transmission gate 224 may be constructed using an n-channel metal-oxide-semiconductor transistor ("nMOS") 236 and a p-channel metal-oxide-semiconductor ("pMOS") transistor 238.

Transmission gates 226 and 224 are placed between the input stage of read amplifier 208 and ground.

When pre-amplifier 202 is placed in write mode, a write current  $I_2$  flows through write signal path 214 to activate write coil 210. As noted above, write current  $I_2$  causes a magnetic field to be generated between write signal path 214 and read signal path 216. This action, in turn, causes an induced voltage  $VL_1$  to occur on the read signal path 216 by mutual inductance. This voltage  $VL_1$  will cause current to flow through the GMR sensor. The write driver 206 will also induce a voltage potential  $VC$  between the write signal path 214 and the read signal path 216. This will cause a current  $IC$  to flow into the read signal path 216 by mutual capacitance. The current induced into the read signal path 116 may then flow into the GMR sensor. Current that flows into the GMS sensor,  $Imr$ , may damage the GMR sensor 212.

When pre-amplifier 202 is placed in write mode, transistors 232, 234, 236, and 238 within the transmission gates 224 and 226 in shunt path 220 are placed in saturation or turned "on". Once saturated, shunt path 220 has a low drain-to-source channel resistance. The lowered resistance path of shunt path 220 provides a path for currents induced in read signal path 216, such as induced by potential  $VL_1$  and induced current  $IC$ , to be shunted into the ground. This current then flows away from the GMR sensor 212 into shunt path 220. This process mitigates, or limits, the amount of current that potentially could flow into GMR sensor 212. Sensor current  $Imr$  may not be eliminated, but it should be reduced to levels low enough to not cause damage. Further, shunt path 220 may provide electro-static discharge ("ESD") protection for GMR

sensor 212. Transistors 232, 234, 236, and 238 within transmission gates 224 and 226 may implement a MOS structure to provide the ESD protection.

The status of the shunt structure depicted in Fig. 2 correlates 5 to the operation mode of pre-amplifier 202. Specific modes will turn shunt path 220 on by saturating transistors 232, 234, 236, and 238 within transmission gates 224 and 226. Table 1 shows an example of operating modes and shunt path 220 status. When shunt path 220 is on, the input resistance to read amplifier 208 is reduced.

Mode	R/Rn input Resistance	MOS Shunt
Read	220	Off
Pre-read	200	Off
Write	10	On
Idle	10	On/Off
Sleep	200	Off
Unselected	10	On
Test	N/A	Off

10

**Table 1**

For a voltage sense reader architecture as depicted in Fig. 2, the input impedance of the read amplifier 208 can be as large as 200 ohms whenever the read input stage is biased on. When the read input stage is biased off, the input impedance of the read amplifier 15 208 can be very large, such as 1000 ohms. A large impedance for read amplifier 208 repels induced current IC into read signal path 216. The large impedance is applicable for reader architectures that clamp the input stage by reverse biasing the input stage transistors

DE98106621-D00230

base-emitter junction that is the first stage within the read amplifier, this is the action of turning off the read amplifier 208. Shunt path 220 coupled to ground may reduce this impedance down to about 10 ohms or less, depending on the silicon process that is 5 used to fabricate the parts for shunt path 220. The lower impedance of shunt path 220 draws induced current IC from read signal path 216.

To illustrate the advantages of the disclosed embodiments, the following example is given. Write driver 206 produces a common-mode output voltage signal of 1 volt peak-to-peak at the write driver outputs when connected to the write signal path 214 of the flex 225. The frequency response of this common-mode write signal will have a peak magnitude at a frequency of 0.5 GHz, associated with the first harmonic of the (Non-Return to Zero) write data at a data rate 10 of 1, 000, 000, 000 bits per second. A distributive capacitive coupling of 3 picofarads ("pF") may exist between read signal path 216 and write signal path 214. The magnitude of the electric field coupling from write signal path 214 into read signal path 216 may 15 be estimated by the equation  $IC = C dv_c/dt$ . The value of  $dv_c/dt$  may 20 be the fundamental frequency of the common-mode write signal. Specifically, the frequency may be characterized by  $1 \text{ volt} / (1 / 0.5 \text{ GHz}) = 1 \text{ volt} / 2 \text{ nanoseconds} = 0.5e9$ . The magnitude of the electric field induced current for a sine wave write data signal at this frequency may be  $IC = 3\text{pF} * 0.5e9 = 1.5 \text{ millamps}$ .

25 The induced current IC may be considered a single current source injected into one of read signal path 216 interconnect lines at a point halfway between the input for read amplifier 208 and GMR sensor 212. The induced current IC divides between the two

resistances at either end of the lines of read signal path 212. Induced current IC may divide following the current divider relations, or:

$$I_{mr} / IC = R_{in} / (R_{in} + R_{mr}), \text{ and}$$

5            $I_{in} / IC = R_{mr} / (R_{in} + R_{mr}).$

I<sub>in</sub> is the current that enters the reader inputs for pre-amplifier 202. Solving for the current I<sub>mr</sub> for the case of R<sub>in</sub> = 10 ohms, R<sub>in</sub> = 200 ohms, and R<sub>in</sub> = 1000 ohms when R<sub>mr</sub> = 50 ohms, the different effects of the lower resistance may be seen in Table 2.

R <sub>IN</sub>	i <sub>MR</sub>
1-kOhm	1.43mA
200-Ohms	1.2mA
10-Ohms	0.25mA

10

Table 2

Thus, according to the example given above, the sensor current I<sub>mr</sub> may be reduced by about 83% when a 10 ohm current shunt path 220 is provided at the reader inputs of pre-amplifier 202. The analysis is independent of the exact value of the lumped mutual capacitance. Rather it is based on the ratio of the shunt resistance to the resistance of the GMR sensor. Therefore, current in GMR sensor 212 may be reduced without the need for additional components or increasing the area or costs of the signal paths from pre-amplifier 202 and recording head 204.

15           The sensor current may be limited within a read sensor according to the present invention. The sensor current may be derived from an induced current within a read signal path coupling

the read sensor to a read amplifier. The induced current is shunted away from the read sensor by a shunt path coupled to the pre-amplifier enclosing the read amplifier. The shunt path has a low resistance, such as about 10 ohms, to limit the induced current

5 flowing to the read sensor.

It will be apparent to those skilled in the art that various modifications and variations can be made in a probe head of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention covers the

10 modifications and variations of this invention provided that they come within the scope of any claims and their equivalents.

05040563 "042301"